



**CENTURION UNIVERSITY OF TECHNOLOGY AND MANAGEMENT, ODISHA  
FDP**

**on**  
**“VLSI Design using Cadence EDA Tool”**  
**(22<sup>nd</sup> to 24<sup>th</sup> May, 2018)**  
**REPORT**

**NAME OF THE PROGRAM: “VLSI Design using Cadence EDA Tool”**

**DATE AND VENUE:** 22<sup>nd</sup> to 24<sup>th</sup> May, 2018, Centurion University of Technology and Management (CUTM), Bhubaneswar campus.

**ABOUT RESOURCE PERSON:** Dr. Ajay Somkuwar is working as a Professor in Department of Electronics & Communication Engineering, MANIT Bhopal. He has vast research experience on research areas like signal processing, bio-signal, VLSI signal processing. He has received so many awards and also published articles in many international and national journals.

**ABOUT THE SESSION:** . The main sessions by Dr. Ajay Somkuwar started at 11.00 AM. In the entire program he has covered the basics and advanced information about the VLSI Design using Cadence EDA Tool.

**OBJECTIVES:**

1. To impart knowledge on VLSI Design using Cadence EDA Tool to the participants
2. To train participants on developing VLSI design using Cadence EDA Tool

**OUTCOME:** T By the end of the faculty development program, the participants got an overall idea about VLSI design creation using the Cadence EDA tool. Certificate of participants were distributed all the participants.

No. of Participants: 12

22, 23, 24, May - 2018

Attendance Record - FDP on VLSI Design using Cadence EDA Tool

| SNo  | Name of the Participant | Day 1   | Day 2   | Day 3   | Participant Signature | Remarks      |
|------|-------------------------|---------|---------|---------|-----------------------|--------------|
|      |                         | 22.5.18 | 23.5.18 | 24.5.18 |                       |              |
| ✓ 01 | Dr. S.K. Maw            |         |         |         |                       | P<br>ECE     |
| ✓ 2  | S.P. Jena               |         |         |         |                       | P<br>ECE     |
| ✓ 3  | Rusmita Das             | RDas    | RDas    | RDas    | RDas                  | P<br>ECE     |
| ✓ 4  | Nutan Das               | N Das   |         |         |                       | P            |
| ✓ 5  | Sambhu Das              |         |         |         |                       | P<br>ECE     |
| ✓ 6  | Siba K. Panda           |         |         |         |                       | P<br>Physics |
| ✓ 7  | Nibedita Nayak          | Nayak   | N Nayak | N Nayak | N Nayak               | P            |
| ✓ 8  | Sourabh Das             | S. Das  | S. Das  | S. Das  | S. Das                | P<br>ECE     |
| ✓ 9  | Debasmita Das           |         |         |         |                       | P<br>ECE     |
| ✓ 10 | Subrat K. Pradhan       |         |         |         |                       | P<br>ECE     |
| ✓ 11 | Jayshankar Pradhan      |         |         |         |                       | P<br>ECE     |



# WORKSHOP ON

## VLSI Design using Cadence EDA Tool

22-05-2018 to 24-05-2018

*Organized by :*

**Resource Person**  
Dr. Ajay Somkuwar  
Dept. of Electronics & Comn. Engineering  
MANIT Bhopal

Centurion University of Technology and  
Management

centurion university of technology and management  
*Shaping Lives... Empowering Communities...*

Handwritten signature of Prabhat K. Pattnaik in black ink.

Prabhat K. Pattnaik  
FDP Coordinator

Handwritten signature of Dr. Prasanta Ku. Mohanty in black ink.

Dr. Prasanta Ku. Mohanty  
Dean Academic