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B. Sc  
BSIT1102

1<sup>st</sup> Semester Examination (2016 Syllabus): Dec 2016

COMPUTER SYSTEM ARCHITECTURE

Full Marks: 30

Time: 2 Hrs.

*Answer Question No.1 which is compulsory and any Five from the rest.*

*The figure in the right hand margin indicates marks.*

PART – A

[5x1=5]

1. a) What is a multiplexer?
- b) What is BUS?
- c) What do you mean by programmed I/O?
- d) What is ROM?
- e) What is pipeline?

PART – B

[5x5=25]

2. What is a flip flop? Explain any one of the flip flop in details.
3. Write an algorithm for the division operation for integers.
4. What is an instruction? Describe the different instructions cycles.
5. Differentiate between RISC and CISC.
6. What is memory unit? Describe the memory hierarchy in details. What is a cache memory?
7. Describe the process of direct memory access.
8. Write short notes on any two of the followings.
  - a. Associative memory.
  - b. 1's complement.
  - c. Interrupts.
  - d. Sequential circuits.